

Practitioner's Docket No.: 830_012

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: Richard REYNOLDS, Simon BROOM and Paul
BARRETT

Ser. No.: 10/758,176

Group Art Unit: 2857

Filed: January 15, 2004

Examiner: Jeffrey R. West

Confirmation No.: 4849

For: QUALITY ASSESSMENT TOOL

RULE 132 DECLARATION

I, Simon Broom, hereby declare and state that:

1. I am one of the inventors named in the above-identified patent application.
2. I received a MEng Degree in Electronic Systems Engineering from the University of York in 1995. I have been employed by Psytechnics Ltd, the assignee of the present application, since February 2001. During my employment at Psytechnics I have been involved in the design and development of software for voice over IP monitoring and quality assessment applications.
3. I am familiar with the prosecution history of the above-identified application, and have reviewed the Office Action mailed May 19, 2006.
4. The specification provides, on page 12, lines 10-15, that the lt_jitter is calculated at step 94 (Fig. 8) according to the equation:
$$lt_jitter = (lt_jitter \times P) + (abs(jitter) \times (1 - P)).$$
5. When taken in the context of the flow chart provided in Fig. 8, one skilled in the art would have understood that the determination of lt_jitter is an ongoing, iterative process updated at every cycle based on a newly calculated value of jitter.

6. One skilled in the art would also have understood that Fig. 8 represents a computer program that can be started and stopped at the direction of a user. As is typical in the art, the calculations would begin with a value for lt_jitter of 0. The inventive process described in Fig. 8 will function normally when run as a typical computer program using a beginning value of 0 for all variables to be calculated.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

6th December 2006
Date



Enclosures: